

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Norikatsu TAKAURA et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PRODUCTION
METHOD THEREOF

* * *

PRELIMINARY AMENDMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-
identified patent application as indicated below.

IN THE SPECIFICATION:

Page 1:

After the title, insert the following section:

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a division of Application No.
10/164,008 filed June 7, 2002.